

HP2-T16

HP

Industry Standard Architecture and Technology

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Exam Name:	Industry Standard Architecture and Technology		
Exam Type:	HP	Exam Code:	HP2-T16
Certification:	Accredited Platform Specialist (APS)	Total Questions:	158

Question: 1

What is provided by the serial port hardware interface for managing network devices?

- A. Scalability
- B. Ability to offload functions from the host
- C. Well-defined communications standards
- D. Caching and advanced functions

Answer: B

Question: 2

Which statement is true about the installation of DIMM memory modules?

- A. A bank of DIMMs cannot contain DIMMs of mixed size and speed.
- B. DIMMs with gold pins can be used on system boards with gold planet and tin plated contacts.
- C. DIMMs with 144 pins provide error checking and correcting capabilities.
- D. Performance defaults to the highest DIMM speed.

Answer: A

Explanation:

Industry Standard Architecture - Student Guide 2 - Page 118: Depending on the server model and memory technology, installations might require that memory be added in banks of four DIMMs. Mixing of 50ns and 60ns memory is permitted; however, each bank of four DIMMs must contain the same size and speed DIMMs.

Question: 3

Which statement is true about PCI Express architecture?

- A. Data is sent serially.
- B. PCI Express utilizes more pins than PCI-X.
- C. PCI Express transfers data in half-duplex.
- D. Data is sent unparallel.

Answer: A

Question: 4

Your server is running Windows 2003 and you are experiencing network performance issues. What can you do to determine if the bottleneck is the network card?

- A. Replace the NVRAM on the network card.
- B. If the server contains multiple network cards, remove all but one card.
- C. Run the system monitor applet to monitor the network throughput.
- D. Add NVRAM to the network card.

Answer: C

Question: 5

How many address lines does an Intel Xeon processor use, and what is the maximum amount of accessible, addressable memory?

- A. 32 address lines; 64GB addressable memory
- B. 36 address lines; 4GB addressable memory
- C. 36 address lines; 64GB addressable memory

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D. 64 address lines; 64GB addressable memory

Answer: C

Explanation:

Advantages of EM64T 64-bit allows an installation of up to 16 EB (exabyte) of RAM; however, current Celeron D, Pentium 4, and Xeon CPUs have 36 address lines, which can support 64 GB of RAM , while Xeon DP CPUs can hold up to 1 TB (terabyte)

Question: 6

What must you check prior to adding another processor to an existing system. (Select three)

- A. Amount of memory in the system
- B. Compatibility of the new process with existing processors
- C. Firmware requirements for the new processor
- D. Number of users currently logged into the system
- E. Number of processors the operating system supports
- F. Weight of the new processor

Answer: B, C, E

Question: 7

Which events could require a firmware update? (Select two)

- A. Downgrading memory
- B. Re-installing the operating system
- C. Adding support for larger, faster drives
- D. Adding virtual machines to a server
- E. Removing an existing processor
- F. Adding plug and play support

Answer: C, F

Explanation:

Industry Standard Architecture - Student Guide 2 - Page 177 It may be necessary to upgrade the system BIOS or firmware for one or more of the following reasons:

- To support new hardware or features on the server
- To correct bugs discovered in the BIOS
- To fix a security hole in the BIOS
- To add support for newer or faster processors
- To add Plug and Play support
- To add support for larger or faster hard drives
- To add support for special removable drives, such as LS-120 or ZIP drives

Question: 8

Which statements are true about active and passive cooling systems? (Select two)

- A. A passive cooling system uses only heat sinks.
- B. An active cooling system uses only heat sinks.
- C. An active cooling system adds devices such as fans.
- D. A passive cooling system adds devices such as fans.

Answer: A, C

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